

REDUCING INDUCTANCE OF A CAPACITOR

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ABSTRACT

5 The inductance of the capacitor is reduced by connecting the capacitor directly to a
via. In one embodiment inductance of a capacitor is reduced by a plurality of via, the number
of via greater than the number of electrical couplings from the voltage pad to the voltage
plane. In one embodiment the capacitor has a ground pad of a minimum size. In another
embodiment the capacitor is electrically coupled to a trace having a length reduced to
10 minimize inductance.